

# **Review on Isolated DC-DC Converter Fed DC Motor for Bidirectional**

# **Electric Vehicle**

## <sup>1</sup>Hiral A. Vaja and <sup>2</sup>Monika D. Patel

<sup>1</sup>Department of Electrical Engineering, M. Tech student, Atmiya University, Rajkot, India <sup>2</sup>Department of Electrical Engineering, Asst. Professor, Atmiya University, Rajkot, India E-mail: <sup>1</sup>hiralvaja001@gmail.com, <sup>2</sup>mdpatel@aits.edu.in

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ABSTRACT - This paper presents a review on isolated dcdc converter fed dc motor for Bidirectional Electric Vehicle. An Isolated dc-dc converter is implemented through bi-directional full bridge converter having transformer which segregate input and output. The input side is the main battery side with relatively high voltage up to 310V and the output side with low voltage of 220V to achieve step down voltage power conversion. A dc-dc converter developed is composed with two stages inverter and rectifier stage. Sinusoidal pulse width modulation technique is used for triggering the switches. This paper presents switching technique applied in fullbridge converter. This technique reduces the over voltage across semiconductor devices. Simulation results are verified by MATLAB/Simulink.

**Keywords:** Isolated DC-DC Converter; Electric Vehicle; power converter; Isolation Transformer; Separately Excited DC Motor; PWM technique; snubber less converter.

## 1. INTRODUCTION

Researchers have studied diverse techniques to enhance performance and strength density of EV chargers [1]. The dc output can be regionally generated by using renewable power resources such as sun PV gadget, wind energy technology, fuel cell, and so on, and can be linked to a commonplace dc bus. In DC applications, some new technologies, e.g., micro-grids, renewable resources, power storages and solid state transformers are primarily based on power electronics converters.

Among them, the isolated bidirectional DC-DC converter is one of the most famous [2-3]. Lots of electric equipment in automobile employs power electronics technology with semiconductor power devices. To respond above demand, electronics with higher switching frequency is one of the key technology[4]. Traditionally, the isolated bidirectional DC-DC converter is one of the most popular converter. Bidirectional converters are appropriate for dc power flow in both directions. Isolated DC-DC converters have excessive voltage isolation from guite a few hundreds to thousand volts relying on the kind of standards. In this review, two different configurations are discussed. fig. 1 is Block diagram of an isolated bidirectional DC-DC converter.

There are two configurations, one is full-bridge inverter and another is single-phase rectifier. Two converter connect with isolation transformer via interconnected DC link.



isolation transformer

Fig.1 Block Diagram of the Full-Bridge Converter.

# 2. OPERATION AND CONTROL PRINCIPLE

### 2.1 Converter Topology

Bidirectional dc-dc converters can be divided into two parts: (1) isolated and (2) non-isolated converters. The isolated converter increases system safety and reliability even though it is more expensive than the non-isolated Commonly, single-phase inverters use half converter. bridge or full bridge configuration. Mostly, full-bridge configuration is used in high-power applications due to its low voltage and current stress, minimum VA rating of the high-frequency transformer, and minimal ripple currents at the output filter.[5]. Fig.2 illustrate an isolated bidirectional DC-DC step down converter, as mentioned in earlier circuit is divided into two parts. One is full bridge inverter and another is single phase Rectifier. Two converter connected with isolation transformer via interconnected dc link.



Fig.2 Full-Bridge isolated DC-DC Converter.

The primary side is the main battery side with relatively high voltage up to 310V and secondary side is output side with low voltage of 220V. To achieve step down voltage DC DC converter is composed with two stage inverter and rectifier. When a switch turns ON and OFF alternatively, charged capacitor work as the energy storage devices and then after some time release energy. Charge capacitor energy is now delivered to the load, because of this ripple generate at the output side. Ripple magnitude is dependent on the capacitor, load resistance and switching frequency. Constant output voltage required for all applications, so ripple magnitude should be minimum. There are various methods for minimizing ripple magnitude. Out of all the method PWM method are used for minimizing the output ripple magnitude [6].

For simplifying the circuit following factor should be considered. 1) All semiconductor devices and an isolation transformer considered ideal; 2) The dead band between the switches represents a time that is small enough to neglected.

#### 2.2 Working principle

Assuming that  $V_{in}$  is the input and  $V_o$  is the output, the circuit operates as voltage reducer and acts like non-isolated buck converter in four stages. The principle of the topology is introduced in this mode. the working process of each cycle is divided into four modes. As shown in Fig. 3, dc supply  $V_{in}$  powering the bi-directional converter. All the switches on the primary side are gated at duty ratio 0.77 Fig.3 show the various stage of operation during one switching time period T. fig.4 show the theoretical waveforms of converter. Where, VT is transformer across secondary voltage.

(1) Mode 1 [ $t_0$ - $t_1$ ]: In this mode at interval [ $t_0$ - $t_1$ ] switch S<sub>1</sub> and S<sub>4</sub> are turned ON. While S<sub>2</sub> and S<sub>3</sub> are turned OFF. A V<sub>in</sub> voltage appears across the primary winding and Secondary winding of transformer side semiconductor diode D<sub>2</sub> and D<sub>3</sub> are forward bias and provide rectification to load as a result of charging inductor L and capacitor C.

Assume a  $V_{\rm in}$  voltage across the block switch at primary side while reverse diode present voltage equal to  $V_o.~V_L$  is voltage across inductor and  $i_L$  is current through inductor.

$$V_L = V_{in} - V_0 \tag{1}$$

 $i_L = i_C - i_0 \tag{2}$ 





(2) Mode 2 [ $t_1$ - $t_2$ ]: In this mode, the Switch S<sub>2</sub> and S<sub>4</sub> are turned ON. During this mode Zero voltage is given to transformer no power transfer to primary side. The energy stored in inductor result in freewheeling of the current equally through the diode D<sub>2</sub> and D<sub>3</sub> due to the excess current at the transformer switch S<sub>2</sub> and S<sub>4</sub> is turned On, while S<sub>1</sub> and S<sub>3</sub> is turned OFF. It is the best possible way to avoid over voltage across the blocked semiconductor device.

$$v_L = -v_0 \tag{3}$$

$$i_L = i_C - i_0 \tag{4}$$

**TABLE I** 

STAGES OF OPERATION OF THE CONVERTE

ON BUCK MODE

State S	Switching interval			
	t <sub>o</sub> -t <sub>1</sub>	$t_1$ - $t_2$	t <sub>2</sub> -t <sub>3</sub>	t <sub>3</sub> -t
S <sub>1</sub>	ON	OFF	OFF	OFF
S <sub>2</sub>	OFF	ON	ON	ON
S <sub>3</sub>	OFF	OFF	ON	OFF
S <sub>4</sub>	ON	ON	OFF	ON
D <sub>1</sub>	OFF	OFF	ON	ON
D <sub>2</sub>	ON	ON	OFF	OFF
D <sub>3</sub>	ON	ON	OFF	OFF
D <sub>4</sub>	OFF	OFF	ON	ON

(3) Mode  $[t_2-t_3]$ : In this mode, a switch  $S_2$  is continuously turned ON at t2 while switch S1 and S4 are turned OFF and presents a voltage equal to V<sub>in</sub>. The operation is similar to that during interval t<sub>0</sub>t<sub>1</sub>. but now negative voltage applied to the transform. In this mode diode D<sub>1</sub> and D<sub>4</sub> conducts and provide secondary side rectification while D<sub>2</sub> and  $D_3$  are blocked across the voltage equal to  $V_0$ . Inductor current increase again as the voltage across inductor increases so, inductor and capacitor charged again. for this mode equation 1 and 2 are valid.

(4) Mode [t<sub>3</sub>-t]: The converter operation during this interval is closely resembles to the interval of  $(t_1-t_2)$  again Zero voltage applied to the transformer.  $S_2$ ,  $S_4$  remains On and  $S_1$  and  $S_3$  are turned OFF. An inductor and capacitor release the energy. Diode  $D_1$  and  $D_4$  are in conduction. Equation 3 and 4 are valid. For sufficient operation of converter average voltage of transformer is should be zero. This is the best way to avoid core saturation.

$$V_{L} = (V_{in} - V_{0})DT + (-v_{0})(1 - D)T$$
  
= 0 (5)

Note that output voltage depends on only the input and duty ratio D. If the input voltage fluctuates the output voltage can be regulated by adjusting the duty ratio appropriately.

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It must be noted that during the interval  $[t_0-t_1]$ and [t<sub>2</sub>-t<sub>3</sub>] an inductor and capacitor is charged due to this instances are referred to as T<sub>ON</sub>. Interval  $[t_0-t_1]$  and  $[t_2-t_3]$  are equal for this reason, the duty cycle of converter is  $D = \frac{2 \cdot T_{0n}}{T}$ 

#### 2.3 Design of LC Filter

The equation that determine output filter is found according to theoretical analysis presented in [7] [8].

Inductance is given by,

$$L = v_L \cdot \frac{\Delta t}{\Delta i_L} \tag{6}$$





In mode 2 and 4 converter is turned OFF so voltage across inductor is V<sub>o</sub>. As stated in equation 3 this condition occurs in mode 2 and 4. For the time referring to (1-D) T/2. Due to this it is possible to replace the voltage in inductor  $v_L$  and  $\Delta t$  equation (6).

$$L = \frac{v_0(1 - D)}{2 \cdot f \cdot \Delta i_L}$$
(7)

Where, f is switch frequency. The capacitor value is derived by charge variation given by,

$$\Delta Q = C \cdot \Delta v_C \tag{8}$$

In converter capacitor is charged  $1/_4$  period of operation and the time of current is equal to  $\frac{\Delta i_{L2}}{2}$  so, equation (6) is given by,

$$\Delta Q = \frac{1}{2} \cdot \frac{T}{4} \cdot \frac{\Delta i_L}{2}$$
$$= \frac{T \cdot \Delta i_L}{16} \qquad (9)$$

From equation (7) capacitor value is,

$$C = \frac{\Delta Q}{\Delta v_{c}} = \frac{T \cdot \Delta i_{L}}{16 \cdot \Delta v_{c}}$$
(10)

Equation (7) is that occurs at the moment when converter is turned OFF. In result time period is (1-D) T/2. And  $\Delta i_L = \frac{v_0}{r}$ 

Therefore, the capacitor value is

$$C = \frac{v_0(1 - D)}{32 \cdot \Delta v_C \cdot L \cdot f}$$
(11)

The design of converter has the following parameter (Assuming all ideal component).

IADLE II: SPECIFICATIONS OF THE SIMULATION	TABLE II: SP	ECIFICATIONS OF	THE SIMULATION
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Parameter	Value
DC input voltage (Vin)	310v
DC output	240v
voltage (Vo)	
duty cycle	0.774
Transformer	1KVA
Ripple voltage $\Delta v_{c}$ (%)	12
Ripple current $\Delta i_L$ (%)	0.833
Switch frequency(f)	20kHZ
Inductor(L)	2.1026mH
Capacitor(C)	3.3630mF
DC motor	240v,5HP,1750rpm,
	4KW

In Fig. 4(a) dc voltage is applied to inverter. By using SPWM method. Switching provided to the high frequency isolated transformer. L and C filter is used to eliminate ripple in output. The operation of isolated bi directional dc-dc converter has been verified through the simulation result. It should be noted that simulation is done only for motoring mode.





Fig. 4(b) Simulated result of the Gate pulses





Fig. 4(b) Simulation model of an isolated dc-dc converter

Fig.4(c) show the switching pulse wave form of  $S_1$ ,  $S_2$ ,  $S_3$  and  $S_4$ . Fig. 4(c) shows that switch voltage wave forms of bridge inverter when the 310V is applied.





Fig.4(c)Simulated result of the switching voltage





Fig. 4(d) is the wave form of the output voltage of



steady state transformer when circuit operate from 310V to 220V. If the input voltage is changed the output voltage varies according to change in voltage.



fig. .4(f) capacitor voltage

Fig.4 (e), (f) shows the inductor current and capacitor voltage when the input voltage is 310V. It can be seen that the dynamic performance is good.





# 4. CONCLUSION

This paper present analysis and design of isolated bi directional converter. Firstly, the topology and working principle of converter introduced and then design of converter is discussed. This configuration is providing low ripple voltage and current. For this method SPWM technique is used for minimizing the output ripple magnitude.

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fig.4(i) field current of dc motor



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