

# A Comparative Analysis of Cuk and Buck Boost Converter for PFC in an Induction Motor

Karthika Nair<sup>1</sup>, Prof. A S Shajilal<sup>2</sup>

<sup>1</sup>PG Scholar, Department of Electrical and Electronics Engineering, Mar Baselios College of Engineering and Technology, Kerala, India.

<sup>2</sup>Professor, Head of Department, Department of Electrical and Electronics Engineering, Mar Baselios College of Engineering and Technology, Kerala, India

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**Abstract:** Power factor correction is essential for ac – dc power supplies because manufactures are forced by the regulatory agencies to minimize harmonic problems. Traditionally we use various methods for power factor correction. This paper deals with a comparative analysis of Cuk and buck boost converter for power factor correction in an Induction motor. Conventional converters uses rectifier circuits with power factor correction but the output voltage is limited. For high controlled voltage range buck boost or Cuk converter should be needed. The performance of the system is evaluated using MATLAB/Simulink environment.

**Keyword:** Power Factor Correction, Cuk Converter, Buck Boost converter, Induction Motor.

## 1. INTRODUCTION

Now a days 75% of the electrical machines used in industrial purposes are Three Phase Induction Motors. For earlier industrial applications dc motors were widely used. But many low and medium voltage applications induction motor drives are mainly used because they have rugged construction, robust, cheaper cost and maintenance free due to the absence of commutators, slip rings and brushes. Induction motor can be operated in any environmental condition such as polluted and explosive because they do not have brushes causes sparking problems. These behavior of induction motor make them more prominent in domestic and industrial applications.

Power factor correction (PFC) is needed for ac – dc power supplies in order to minimize the harmonic problems. There are many techniques available for these Power Factor Correction. One of the main technique is adding the passive filter elements like inductor and capacitor to the diode bridge rectifier with input filter. Due to the size of inductors and capacitors having low frequency causes very bulky converters. The other technique is adding an active Power Factor Correction, which has power electronic devices to shape the input line current as sinusoidal with less disturbance to meet the harmonic standard specification.

Diode bridge rectifier, buck, boost, buck –boost and cuk converters are used for the implementation of this stage [1].

A bridgeless cuk converter configuration was proposed by V Krishnan et al [2]. In this configuration the input current is highly pulsating which will cause high ripples in output voltage. Wu et al. [3] have implemented a cascaded buck-boost converter, which makes use of two switches for PFC operation. This gives high switching losses within the front end converter due to double switch and reduces the performance of overall system. Ho et. al. [4] have proposed an active power factor correction (APFC) scheme which makes use of a PWM switching of VSI and subsequently has large switching losses. Gopalathnam et. al. [5] have proposed a single ended primary inductance converter (SEPIC) as a front end converter for PFC with DC link voltage control approach, but utilizes a PWM switching of VSI which has high switching losses. A novel tri-state boost PFC converter operating in Pseudo-Continuous Conduction Mode (PCCM) was proposed by Fei Zhang et al. [6]. The tri-state boost PFC converter provides wider load range and faster dynamic response. An additional power switch is required to make the boost converter operate in PCCM. Bridgeless configurations of PFC buck-boost, Cuk, SEPIC and Zeta converters have been proposed in [7 - 10] respectively. These configurations offer reduced losses in the front end converter but at the cost of high number of passive and active components. Buso et. al.[11] implemented a high-power-factor fly back Cuk and Sepic rectifiers operating in CCM for power factor correction operation. But the topology is complicated causes uneconomic.

## 2. BLOCK DIAGRAM

Fig. 1, shows the block diagram of the proposed high power factor inverter fed induction motor drive system with PFC. The main components of a power factor corrected induction motor drive system are a rectifier, Cuk converter, three phase Voltage Source Inverter (VSI), induction motor and the control circuit.

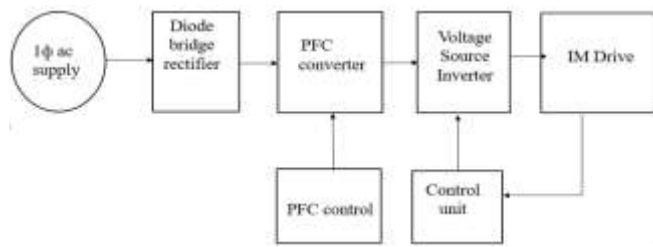


Fig-1 : Block diagram of power factor correction system

The input AC voltage is converted to DC voltage using a bridge rectifier. An LC filter at the output of rectifier helps to reduce the ripple in the output voltage of the rectifier. The output of the bridge rectifier feeds the Cuk converter. The regulated DC link voltage is supplied to a conventional six switch 3 phase VSI which produces an AC voltage that is fed to the induction motor. The power factor correction at the input side of the dc-dc converter is achieved by regulating its output voltage with proper control.

### 3. OPERATION OF CUK CONVERTER

Cuk converter as shown in fig.2, is a type of DC-DC converter that has an output voltage magnitude either greater than or less than the input voltage magnitude. It uses a capacitor as its main energy-storage component. Cuk converter with proper control regulates the output DC link voltage with power factor correction at the input side.

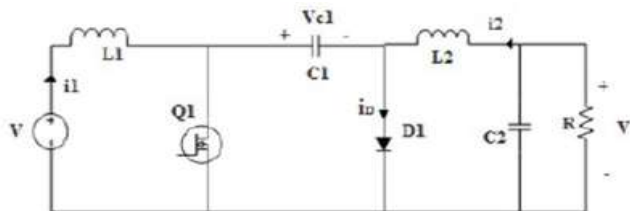


Fig-2: Cuk Converter

Cuk converter operates in both continuous conduction mode (CCM) and discontinuous conduction mode (DCM). In CCM, the current in the inductors ( $L_1$  and  $L_2$ ) and the voltage across intermediate capacitor  $C_1$  remains continuous in a switching period. In DCM operation, the converter can operate in DICM and Discontinuous Capacitor Voltage Mode (DCVM). In the DICM, the current flowing in inductor  $L_1$  or  $L_2$  becomes discontinuous in their respective modes of operation. While in DCVM operation, the voltage appearing across the intermediate capacitor  $C_1$  becomes discontinuous in a switching period.

### 3.1 CCM Operation

When switch  $S_w$  is turned on, inductor  $L_1$  stores energy while capacitor  $C_1$  discharges and transfers its energy to DC link capacitor  $C_d$ . The fig. 3, shows the different intervals in CCM mode.

Input inductor current  $i_{L1}$  increases while the voltage across the intermediate capacitor  $V_{C1}$  decreases.

When switch  $S_w$  is turned off, then the energy stored in inductor  $L_o$  is transferred to DC link capacitor  $C_d$ , and inductor  $L_i$  transfers its stored energy to the intermediate capacitor  $C_1$ . The designed values of  $L_i$ ,  $L_o$  and  $C_1$  are large enough such that a finite amount of energy is always stored in these components in a switching period.

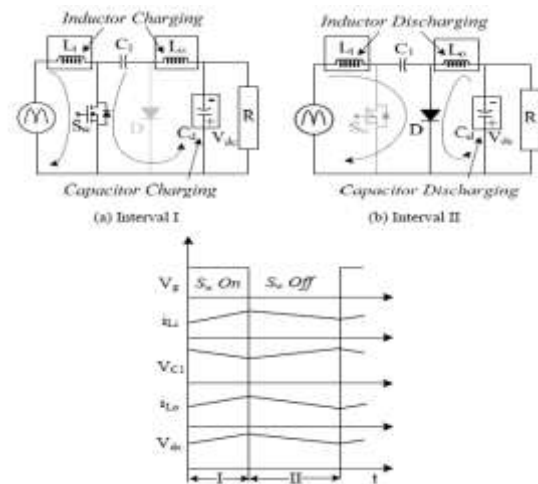


Fig -3: Continuous Conduction Mode

### 3.2 DCM Operation

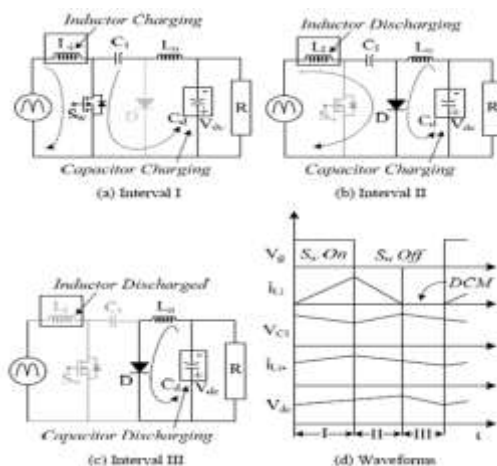


Fig-4: Discontinuous Conduction Mode

When switch  $S_w$  is turned on, inductor  $L_i$  stores energy while capacitor  $C_1$  discharges through Switch  $S_w$  to transfer its energy to the DC link capacitor  $C_d$ . Input inductor current  $i_{L_i}$  increases while the voltage across the capacitor  $C_1$  decreases.

When switch  $S_w$  is turned off, then the energy stored in inductor  $L_i$  is transferred to intermediate capacitor  $C_1$  via diode  $D$ , till it is completely discharged to enter DCM operation. During this interval, no energy is left in input inductor  $L_i$ , hence current  $i_{L_i}$  becomes zero. Moreover, inductor  $L_o$  operates in continuous conduction to transfer its energy to DC link capacitor  $C_d$ . The fig. 4, shows the different intervals in DCM mode.

#### 4. OPERATION OF BUCK BOOST CONVERTER

A buck-boost converter is shown in Fig. 5. The inductor,  $L$  is connected in parallel after the switch and before the diode. A capacitor,  $C$  is connected in parallel with the load. The polarity of the output voltage is opposite to that of input voltage here.

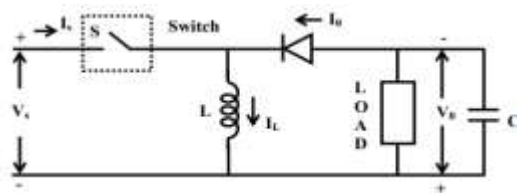


Fig-5: Buck Boost Converter

When the switch,  $S$  is put ON, the supply current  $i_s$  flows through the path,  $V_s$ ,  $S$  and  $L$ , during the time interval,  $T_{on}$ . The currents through both source and inductor  $i_L$  increase and are same, with  $\frac{di_L}{dt}$  being positive. The polarity of the induced voltage is same as that of the input voltage. The equation for the circuit is,  $V_s = L \frac{di_L}{dt}$ . Then, the switch,  $S$  is put OFF. The inductor current tends to decrease, with the polarity of the induced emf reversing.  $\frac{di_L}{dt}$  is negative now, the polarity of the output voltage, being opposite to that of the input voltage. The path of the current is through  $L$ , parallel combination of load &  $C$ , and diode  $D$ , during the time interval,  $T_{off}$ . The output voltage remains nearly constant, as the capacitor is connected across the load. The equation for the circuit is,  $V_s = L \frac{di_L}{dt}$  from which the average value of the output voltage is  $V_o = V_s (T_{on} / T_{off}) = V_s \frac{k}{1-k}$ . The time period is  $T = T_{on} + T_{off}$  and the duty ratio is  $k = (T_{on} / T)$ . For the range  $0 > k \geq 0.5$ , the output voltage is lower than the input voltage, thus, making it a buck converter (dc-dc). For the

range  $0.5 > k \geq 1.0$ , the output voltage is higher than the input voltage, thus, making it a boost converter (dc-dc). For  $k = 0.5$ , the output voltage is equal to the input voltage. So, this circuit can be termed as a buck-boost converter. Also it may be called as step-up/down chopper.

#### 5. SIMULATION RESULTS

This section deals with simulation results of power factor correction of induction motor drive with and without using Cuk and buck boost converter. The specifications of the induction motor of 0.25HP, 400V, 1425rpm 4 pole, 3phase squirrel cage are specified in Table I.

Table -I: Specification of 3 phase squirrel cage induction motor

Parameters	Value
Stator resistance	11.2 $\Omega$
Stator inductance	0.02 H
Rotor resistance	6.67 $\Omega$
Rotor inductance	0.02 H
Mutual inductance	0.3 H

The designed parameters of Cuk converter and buck boost converter to improve power factor correction are given in table II and table III

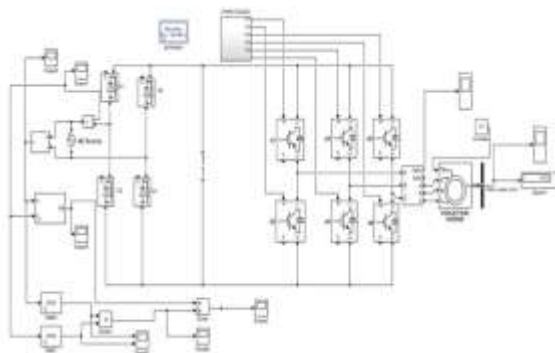
Table-II: Designed Values of Cuk Converter

Parameters	Values
Input inductor	$0.2 \times 10^{-3}$ H
Output inductor	$20 \times 10^{-3}$ H
Input capacitor	$0.1 \times 10^{-6}$ F
Output capacitor	$10 \times 10^{-6}$ F
Filter inductor	$2 \times 10^{-3}$ H
Filter capacitor	$3.3 \times 10^{-6}$ F

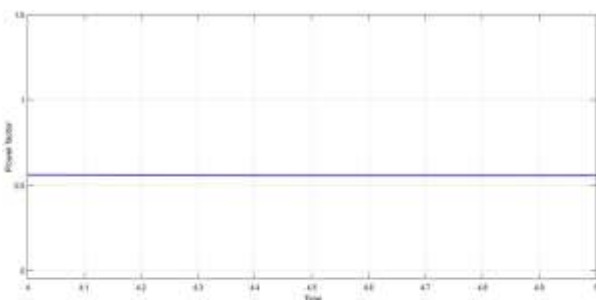
**Table-III:** Designed Values of Buck Boost Converter

Parameters	Values
Input inductor	$2.2 \times 10^{-3}$ H
Output capacitor	$47 \times 10^{-6}$ F
Filter inductor	$2 \times 10^{-3}$ H
Filter capacitor	$3.3 \times 10^{-6}$ F

The presence of power semiconductor switches in adjustable speed drives results in low power factor and high total harmonic distortion. The rectifier at the front end part is the major reason for low power factor. A voltage source inverter fed 3 phase induction motor using a single phase ac supply with front end rectifier is simulated using MATLAB is shown in Fig 6.

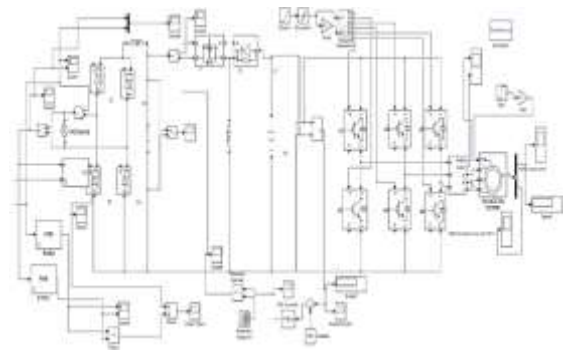


**Fig -6:** Simulink model of induction motor without PFC



**Fig -7:** Power factor in 3 phase squirrel cage induction motor without PFC converter

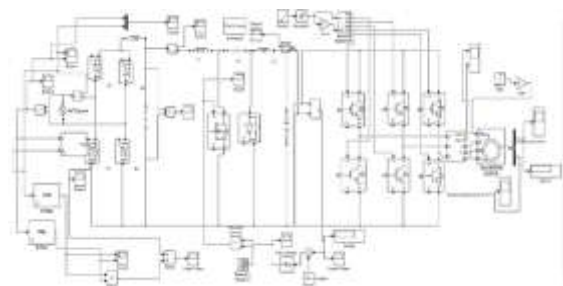
In 3 phase squirrel cage induction motor without using power factor correction converter, the power factor is found to be approximately 0.6 as shown in Fig 7. Power factor correction is achieved using Cuk converter and Buck Boost Converter.



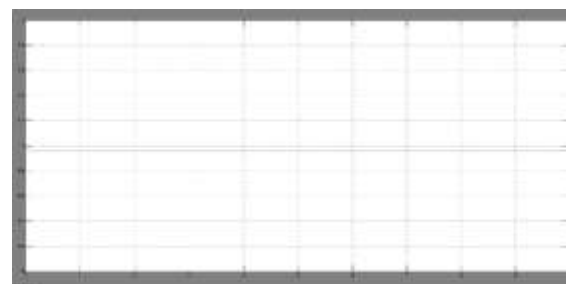
**Fig:8-** PFC of induction motor using Buck Boost converter



**Fig -9:** Power factor in 3 phase squirrel cage induction motor with PFC Buck Boost converter



**Fig -10:** PFC of induction motor using Cuk converter



**Fig -11:** Power factor in 3 phase squirrel cage Induction motor with PFC Buck Boost converter

Power factor correction is achieved using Cuk converter and Buck Boost converter. The converters are designed to meet the specification of induction motor. Proper control is given to regulate the output voltage of converter and to improve the power factor. The control is obtained using a PI

controller. Using the Buck Boost converter the power factor is found to be improved and equal to 0.9 as shown in Fig 9. With the Cuk converter power factor correction is improved and obtained as 0.96 as shown in Fig 11. Comparatively Cuk converter give more power factor correction and low harmonics than Buck Boost converter.

## 6. CONCLUSION

Inverter fed induction motor without power factor correction is simulated and power factor is observed as low. Using a Cuk converter and buck boost converter power factor correction occurred. Cuk converter and buck boost converter are simulated in MATLAB/Simulink model.

## REFERENCES

- [1] Singh, B. N. Singh, A. Chandra, K. Al-Haddad, A. Pandey, and D. P. Kothari, "A review of single-phase improved power quality ac-dc converters," IEEE Transactions on Industrial Electronics, vol. 50, no. 5, pp. 962–981, Oct 2003.
- [2] R. Krishnan, D. Diamantidis, and S. Lee, "Impact of power factor correction on low power inverter-fed induction motor drive system," in Proceedings of the 26th Annual IEEE Power Electronics Specialists Conference, 1995. PESC '95, vol. 1, Jun 1995, pp. 593–598.
- [3] C.-H. Wu and Y.-Y. Tzou, "Digital control strategy for efficiency optimization of a bldc motor driver with vopfc," in Proceedings of the IEEE Energy Conversion Congress and Exposition, Sept 2009, pp. 2528–2534.
- [4] T. Y. Ho, M. S. Chen, L. H. Yang and W. L. Lin, "The Design of a High Power Factor Brushless DC Motor Drive," 2012 Int. Symposium on Computer, Consumer and Control (IS3C), pp.345-348, 4-6 June 2012.
- [5] T. Gopalarathnam and H. A. Toliyat, "A new topology for unipolar brushless DC motor drive with high power factor," IEEE Trans. Power Elect., vol.18, no.6, pp. 1397-1404, Nov. 2003.
- [6] F. Zhang, J. Xu, J. Wang, and H. Yu, "A novel tri-state boost PFC converter with fast dynamic performance," in Proceedings of the 5<sup>th</sup> IEEE Conference on Industrial Electronics and Applications, June 2010, pp. 2104–2109.
- [7] V. Bist and B. Singh, "An Adjustable Speed PFC Bridgeless Buck-Boost Converter Fed BLDC Motor Drive", IEEE Trans. Ind. Electron., vol.61, no.6, pp.2665-2677, June 2014.
- [8] B. Singh and V. Bist, "An Improved Power Quality Bridgeless Cuk Converter Fed BLDC Motor Drive for Air Conditioning System", IET Power Electron., vol. 6, no. 5, p. 902–913, 2013.
- [9] B. Singh and V. Bist, "Power Quality Improvement in PFC Bridgeless SEPIC Fed BLDC Motor Drive", Int. Jr. Emerging Electric Power Sys. (IJEEPS). vol. 14, no. 3, Pages 285–296, 2013.
- [10] V. Bist and B. Singh, "A Reduced Sensor PFC BL-Zeta Converter Based VSI Fed BLDC Motor Drive", Electric Power System Research, vol. 98, pp. 11–18, May 2013.
- [11] S. Buso, G. Spiazzi, and D. Tagliavia, "Simplified technique for high-power-factor flyback Cuk and Sepic rectifiers operating in CCM," IEEE Trans. Ind. Appl., vol.36, no.5, pp.1413,1418, Sep/Oct 2000.

## AUTHORS PROFILE



**Karthika Nair** received her B-Tech degree in Electrical and Electronics Engineering from Sree Narayana Institute of Technology, Pathanamthitta, Kerala in the year of 2018. She is currently pursuing her M-Tech degree in Power Control and drives from Mar Baselios College of Engineering, Thiruvananthapuram, Kerala.



**A.S. Shajilal** received his B-Tech from College of Engineering Trivandrum and M-Tech from IIT Kharagpur. Teaching experience is 30 years and presently working as a HOD (EEE), Mar Baselios College of Engineering, Thiruvananthapuram. His field of interests are power electronics, industrial drives, vector control and solar and wind energy sources