IMPROVED IUPQC CONTROLLER TO PROVIDE GRID VOLTAGE AS A STATCOM

P. BHASKAR¹, M. SUBRAMANYAM², P. NAGENDRA³, J.C. VANNUR SWAMY⁴

***______

ABSTRACT - This project shows an enhanced controller for the double topology of the bound together power quality conditioner (iUPQC) broadening its appropriateness in power-quality compensation, and also in small scale grid applications. By using this controller, past the conventional UPQC power quality highlights, including voltage list/swell compensation, the iUPQC will similarly give reactive power support to direct the loadbus voltage and additionally the voltage at the grid-side bus. In other words, the iUPQC will fill in as a static synchronous compensator (STATCOM) at the grid side, while giving in like manner the conventional UPOC compensations at the load or little scale grid side. Experimental results are given to affirm the new value of the equipment.

1.INTRODUCTION

Irrefutably, control contraptions gadgets have realized astonishing mechanical updates. Notwithstanding, the extending number of intensity equipment driven burdens utilized all around in the business has achieved extraordinary power quality issues. Abnormally, control equipment driven loads for the most part require perfect sinusoidal supply voltage so as to work appropriately, while they are the fit ones for capricious symphonious flows level in the conveyance framework. In this condition, gadgets that can help these disadvantages have been made consistently. A touch of the blueprints consolidate an adaptable compensator, known as the bound together power quality conditioner (UPQC) [l]- [7] and the static synchronous compensator (STATCOM) [8]- [13]. The twofold topology of the UPOC, i.e., the iUPOC, was appeared in [14]– [19], where the shunt Active channel acts as an atmosphere control framework voltage source and the arrangement one as a constrained air framework current source, both at the fundamental recurrence. This is a key point to even more quickly structure the control gains, and despite streamline the LCL channel of the power converters, which licenses upgrading on an extremely fundamental dimension the general execution of the compensator [20]. Nowadays, the STATCOM is, everything considered, utilized for voltage heading [9], though the UPQC and the iUPQC have been picked as react in due request in regards to constantly express applications[21]. In like manner, these last ones are utilized just unequivocally cases, where their generally staggering expenses are legitimized by the power quality improvement it can give, which would be unfeasible by utilizing traditional approaches. By joining the additional accommodation like a STATCOM in the iUPQC gadget, a

continuously expansive situation of employments can be come to, especially if o passed on age in sagacious matrices and as the coupling gadget in matrix tied littler scale frameworks. Thus, persistently, the UPQC controller needs to pick and combine certainly the symphonious voltage and current to be changed. On the other hand, in the iUPQC approach, the arrangement converter carries on as a controlled sinusoidal current source and the shunt converter as a controlled sinusoidal voltage source. This construes it isn't fundamental to pick the symphonious voltage and current to be changed, since the consonant voltages show up frequently over the arrangement current source and the symphonious ebbs and flows stream normally into the shunt voltage source. In real power converters, as the exchanging recurrence gathers, the power rate limit is decreased. Along these lines, the iUPQC offers better designs at whatever point separated and the UPQC if there should arise an occurrence of high-control applications.



Fig 1 Applicability of iUPQC.

2. VOLTAGE SAGS

Voltage lists are huge issues for some ventures, and it is presumably the most squeezing power quality issue today. Voltage droops may cause stumbling and expansive torque tops in electrical machines. Stumbling is caused by under voltage protection or over current protection. These two protections work freely. Vast torque pinnacles may make harm the shaft or equipment associated with the shaft. Some common purpose behind voltage lists are lightning strikes in power lines, equipment disappointments, coincidental contact power lines, and electrical machine starts. In spite of being a short duration between 10 milliseconds to 1 second occasion amid which a reduction in the RMS voltage extent happens, a little reduction in the system voltage can cause genuine outcomes.

2.1 DEFINITION OF VOLTAGE SAGS:

The meaning of voltage droops is frequently set based on two parameters, greatness or depth and duration. However, these parameters are deciphered diversely by different sources. Other critical parameters that depict voltage lists are:

- 1. The point-on-wave where the voltage droops happens, and
- 2. How the phase point changes amid the voltage hang. A phase edge bounce amid a fault is because of the change of the X/R-ratio. The phase edge hop is an issue particularly for power gadgets utilizing phase or zero-intersection switching.

The voltage hangs as characterized by IEEE Standard 1159, IEEE Recommended Practice for Monitoring Electric Power Quality, is —a decline in RMS voltage or current at the power frequency for durations from 0.5 cycles to 1 minute, revealed as the remaining voltage.



Fig 2: Depiction of Voltage Sag

Average values are between 0.1 p.u. and 0.9 p.u., and run of the mill fault clearing times extend from three to thirty cycles relying upon the fault current size and the sort of over current location and interruption. Wording used to portray the extent of voltage list is frequently befuddling. The prescribed phrasing as indicated by IEEE Std. 1159 is -the droop to 20%, || which implies that line voltage is diminished to 20% of ordinary esteem. Another definition as given in IEEE Std. 1159, 3.1.73 is -A variety of the RMS estimation of the voltage from ostensible voltage for a period more noteworthy than 0.5 cycles of the power frequency however not exactly or equivalent to 1 minute. Generally further depicted utilizing a modifier showing the size of a voltage variety (e.g. list, swell, or interruption) and perhaps a modifier demonstrating the duration of the variety (e.g., quick, passing, or temporary) ||. Figure 2.1 shows the rectangular delineation of the voltage hang.

Standards related with voltage hangs are expected to be utilized as reference records portraying single parts and systems in a power system. Both the makers and the purchasers utilize these standards to meet better power quality prerequisites. Makes create items meeting the necessities of a standard, and purchasers demand from the makes that the item agree to the standard.

MATLAB/SIMULINK RESULT

















CONCLUSION

In the enhanced iUPQC controller, the currents synthesized by the series converter are dictated by the normal active power of the load and the active power to give the dcinterface voltage direction, together with a normal reactive power to manage the grid-bus voltage. Thusly, notwithstanding all the power-quality compensation highlights of a conventional UPOC or an iUPOC, this enhanced controller likewise emulates a STATCOM to the grid bus. This new element enhances the pertinence of the iUPQC and gives new arrangements in future situations including shrewd grids and micro grids, including circulated generation and vitality stockpiling systems to more readily manage the inherent inconstancy of inexhaustible resources such as solar and wind power. Regardless of the expansion of one more power-quality compensation include, the grid-voltage control lessens the internal loop coursing power inside the iUPQC, which would permit lower power rating for the series converter. The experimental results checked the enhanced iUPQC objectives. The grid-voltage control was achieved with no load, and also when supplying a three-phase nonlinear load. These results have exhibited a reasonable execution of voltage direction at both sides of the iUPOC, even while repaying harmonic current and voltage awkward nature.

REFERENCES :

- K. Karanki, G. Geddada, M. K. Mishra, and B. K. Kumar, –A modified three-phase four-wire UPQC topology with reduced DC-link voltage rating, || IEEE Trans. Ind. Electron., vol. 60, no. 9, pp. 3555–3566, Sep. 2013.
- **2.** V. Khadkikar and A. Chandra, —A new control philosophy for a unified power quality conditioner

(UPQC) to coordinate load-reactive power demand between shunt and series inverters, || IEEE Trans. Power Del., vol. 23, no. 4, pp. 2522–2534, Oct. 2008.

- K. H. Kwan, P. L. So, and Y. C. Chu, —An output regulation-based unified power quality conditioner with Kalman filters,|| IEEE Trans.Ind.Electron.,vol.59,no.11, pp. 4248–4262, Nov. 2012.
- **4.** A. Mokhtatpour and H. A. Shayanfar, —Power quality compensation as well as power flow control using of unified power quality conditioner,|| in Proc. APPEEC, 2011, pp. 1–4.
- **5.** J. A. Munoz et al., —Design of a discrete-time linear control strategy for a multicell UPQC, || IEEE Trans. Ind. Electron., vol. 59, no. 10, pp. 3797–3807, Oct. 2012.
- 6. V. Khadkikar and A. Chandra, -UPQC-S: A novel concept of simultaneous voltage sag/swell and load reactive power compensations utilizing series inverter of UPQC, II IEEE Trans. Power Electron., vol. 26, no. 9,pp. 2414–2425, Sep. 2011.
- V. Khadkikar, –Enhancing electric power quality using UPQC: A comprehensive overview,|| IEEE Trans. Power Electron., vol. 27, no. 5,pp. 2284–2297, May 2012.
- **8.** L. G. B. Rolim, −Custom power interfaces for renewable energy sources, ||in Proc. IEEE ISIE, 2007, pp. 2673–2678.
- **9.** N. Voraphonpiput and S.Chatratana,—STATCOM analysis and controller design for power system voltage regulation,∥ in Proc. IEEE/PES Transmiss. Distrib. Conf. Exhib. --Asia Pac., 2005, pp. 1–6.
- J. J. Sanchez-Gasca, N. W. Miller, E. V. Larsen, A. Edris, and D. A. Bradshaw, Potential benefits of STATCOM application to improve generation station performance, || in Proc. IEEE/PES Transmiss. Distrib.Conf. Expo., 2001, vol. 2, pp. 1123–1128.

AUTHORS:

1. P. BHASKAR,

MASTER OF TECHNOLOGY in POWER ELECTRONICS to the Jawaharlal Nehru technological university

2. M. SUBRAMANYAM,

Assistant Professor, Department of EEE.

3. P. NAGENDRA,

Assistant Professor, Department of EEE.

4. J.C. VANNUR SWAMY.

Assistant Professor, Department of EEE.