

# Comparative Study of Voltage source inverter with Z source inverter

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**Abstract** -An implementation of the constant V/Hz control strategy for proposed impedance source inverter (Z – source inverter) fed induction motor in closed loop control has emerged as a powerful tool for controlling induction motor drives to achieve high performance and good dynamic response. In this paper, a new control scheme is developed for control of the induction motor drive by ZSI by boosting line voltage, lowering harmonics distortion, maintaining speed of the drive. In this scheme stator voltage is boosted for obtaining desired torque response and maintain constant frequency and speed below rated speed. Simulation results of the proposed Z –source inverter scheme and traditional inverter are presented which compared for their comparative study.

**Key Words:** Z source inverter, Induction motor drive, Harmonics distortion

## 1. INTRODUCTION

Nowadays, energy storage and conversion plays the vital role in the field of distributed generation. Many of them uses converter such as rectifier and inverter. This paper reviews some of traditional inverter topology such as current source inverter and voltage source inverter with their drawbacks hence introducing newer topology as z-source inverter which had discuss further.

### 1.1 CURRENT SOURCE INVERTER

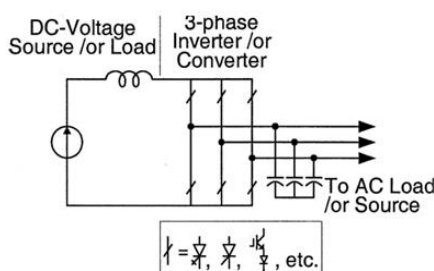


Fig 1: Current Source Inverter

Fig. 1. shows the traditional three-phase current source inverter structure. A dc current source feeds the main converter circuit, a three-phase bridge. The voltage source such as a battery, fuel-cell stack, diode rectifier, or thyristor inverter provides dc current source fed by the dc inductor. The power transistor such as Insulated Gate Bipolar Transistor (IGBT), Silicon Controlled Rectifier

(SCR), Metal Oxide Semiconductor Field Effect Transistor (MOSFET), or a combination of such devices with the series diode is used. These devices act as switching devices according to their switching sequence the three phases AC output was obtain is given to load. For CSI shape of the output voltage is depending on load and output current is independent of load. The CSI is to work as a current source by means of the large series inductor at its output and a current regulation loop.

### 1.1.1 LIMITATIONS OF CSI:

1. The ac output voltage has to be greater than the original dc voltage that feeds the dc inductor or the dc voltage produced is always smaller than the ac input voltage. There-fore, the current-source inverter is a boost inverter for dc-to-ac power conversion and the current source converter is a buck rectifier (or buck converter) for ac-to-dc power conversion. For applications where a wide voltage range is desirable, an additional dc-dc buck (or boost) converter is needed. The additional power conversion stage increases system cost and lowers efficiency.

2. At least one of the upper devices and one of the lower devices have to be gated on and maintained on at time. Otherwise, an open circuit of the dc inductor would occur and destroy the devices. The open-circuit problem by EMI noise mis-gating-off is a major concern of the inverter's reliability. Overlap time for safe current commutation has to be required in the current source inverter, which may causes waveform distortion, etc.

3. The main switches of the current source inverter have to block reverse voltage that requires a series diode to be used in combination with high-speed and high-performance transistors such as insulated gate bipolar transistors (IGBTs).

### 1.2 . VOLTAGE SOURCE INVERTER:

It consists of DC source as input or fully controlled rectifier fed with AC source and followed by a filter or diode bridge rectifier followed by chopper.

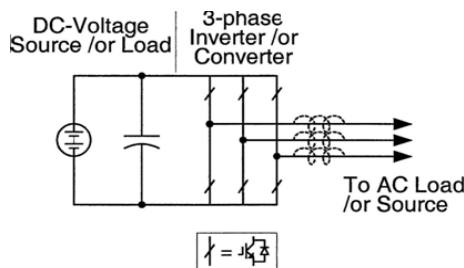


Fig 2: Voltage Source Inverter

The capacitor  $C$  is connected in series with diode. The power transistors such as Insulated Gate Bipolar Transistor (IGBT), Silicon Controlled Rectifier (SCR), Metal Oxide Semiconductor Field Effect Transistor (MOSFET), or combination of such devices with antiparallel diode is used to provide bidirectional current flow and unidirectional voltage.

The output voltage of VSI is smaller than input voltage which can be supplied by the rectifier, fuel cell, battery, photovoltaic array. It is essential to boost that voltage by DC-DC boost converter which reduces reliability as well as electromagnetic interference probably reduced performance of the inverter.

1.2.1 LIMITATIONS OF VSI :

1. Voltage-source inverter is a buck (step-down) inverter for dc-to-ac power conversion. For applications where over drive is desirable and the available dc voltage is limited, an additional dc-dc boost converter is needed to obtain a desired ac output. The additional power converter stage increases system cost and lowers efficiency.

2. The upper and lower devices of each phase leg cannot be gated on simultaneously either by purpose or by EMI noise. Otherwise, a shoot-through would occur and destroy the devices. The shoot-through problem by electromagnetic interference (EMI) noise's mis gating-on is a major killer to the inverter's reliability. Dead time to block both upper and lower devices has to be provided in the voltage source inverter, which causes waveform distortion, etc.

2. Z-SOURCE INVERTER:

The Z-source inverter consists of rectifier unit, impedance network and inverter circuit with three phases AC Induction motor. The input 3 phase AC supply fed to the Rectifier which converts AC Supply to the DC supply. The output of rectifier DC voltage fed to the impedance network. The impedance network consists of two inductors and two capacitors with similar value because circuit is symmetry. The inductors are connected in series and capacitor is connected diagonal arm. Due to two identical inductor and two identical capacitors the

circuit has unique feature to avoid short circuit when the device are in shoot through mode. The switches used in converter is MOSFET anti parallel with DIODE. The impedance network used to boost or buck voltage depend upon the boost factor. This network acts as filter. The output of impedance network fed to the inverter which convert DC voltage to the AC voltage and fed to the AC load (AC motor). The voltage source inverter has six active states and the Z-source inverter has one extra zero state. It has nine permissible states.

2.1 ADVANTAGES OF Z-SOURCE INVERTER

- The z-source inverter works as either voltage source or current source.
- It can be work as Buck-Boost converter. Hence it can give the desired output voltage.
- Its reliability is high due to lower EMI noise and harmonics. The ZSI can work on load either be Inductive, Capacitive or Z-source network.

2.2 MODES OF OPERATION:

MODE 1: The inverter bridge is operating in one of six traditional active vectors. It acting as current source viewed from the DC link. Both the inductor has same value because circuit is symmetry. Both inductors carry the current.

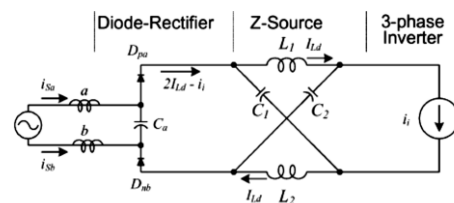


Fig 3: mode 1

MODE 2: The inverter bridge is operating in one two traditional zero vector and shorted through upper or lower two device of inverter via like open circuit from Z-source. In these mode inductor carry current and contribute line current.

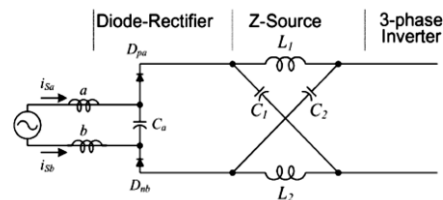


Fig 4: mode 2

MODE 3: The inverter bridge is operating one of seven shoot through state. The shoot through state are generated by shifting the modulation signal of Voltage

Source Inverter. These shoot through state used in switching cycle. The shoot through interval or duty cycle is depend upon the how much voltage is boosted.

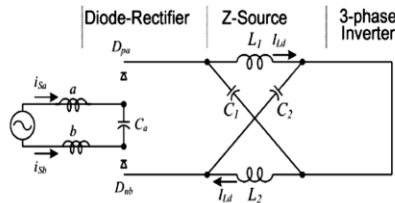


Fig 5: mode 3

### 2.3 ANALYSIS AND DESIGN OF THE IMPEDANCE NETWORK

Assume the inductors ( $L_1$  and  $L_2$ ) and capacitors ( $C_1$  and  $C_2$ ) have the same inductance and capacitance values respectively.

Case I: When inverter is in shoot through zero state ( $T_0$ )

From the above equivalent circuit

$$V_{c1} = V_{c2} = V_c \quad (1)$$

$$V_{L1} = V_{L2} = V_L$$

$$V_L = V_c, \quad (2)$$

$$V_i = 0;$$

Case II: When inverter is in one of the non shoot through zero state ( $T_1$ ):

During the switching cycle  $T$

$$V_L = V_o - V_c \quad (3)$$

$$V_d = V_o$$

$$V_i = V_c - V_L = 2V_c - V_o$$

$$V_i = 2V_c - V_o \quad (4)$$

Where,  $V_o$  is the dc source voltage and

$$T = T_0 + T_1 \quad (5)$$

The average voltage of the inductors over one switching period ( $T$ ) should be zero in steady state

$$V_L = \frac{[T_0 \cdot V_c + T_1 (V_o - V_c)]}{T} = 0$$

$$V_L = \frac{[T_0 \cdot V_c + V_o \cdot T_1 - V_c \cdot T_1]}{T} = 0$$

$$\therefore (T_0 - T_1) \cdot V_c + (T_1 \cdot V_o) = 0$$

$$\frac{V_c}{V_o} = \frac{T_1}{T_1 - T_0}$$

$$V_c = V_o \cdot \frac{T_1}{(T_1 - T_0)} \quad (6)$$

Similarly the average dc link voltage across the inverter bridge can be found as follows.

From equation 4:

$$V_i = 2V_c - V_o$$

$$V_i = 2 \frac{V_o T_1}{(T_1 - T_0)} - V_o$$

$$V_i = V_o \left[ 2 \left( \frac{T_1}{(T_1 - T_0)} \right) - 1 \right]$$

$$V_i = V_o \left( \frac{2T_1 - T_1 + T_0}{(T_1 - T_0)} \right)$$

$$V_i = V_o \cdot \frac{T}{(T_1 - T_0)}$$

$$V_i = V_o \cdot B \quad (7)$$

$$B = \frac{T}{(T_1 - T_0)}$$

$$(8)$$

Where  $B$  is a boost factor.

The average dc link voltage across the inverter bridge can be found as follows.

From equation No. 7

$$V_i = B V_o$$

From equation No 8 substituting for  $B$

$$V_i = \frac{T}{(T_1 - T_0)} \cdot V_o \quad (9)$$

From equation No 6,

$$V_c = V_o \cdot \frac{T_1}{(T_1 - T_0)}$$

$$V_o = V_c \cdot \frac{(T_1 - T_0)}{T_1} \quad (10)$$

Substituting equation No 10 in equation No.9

$$V_i = \frac{T}{(T_1 - T_0)} \cdot V_c \cdot \frac{(T_1 - T_0)}{T_1}$$

$$V_i = \frac{T}{T_1} \cdot V_c$$

$$(\because T = T_1 + T_0);$$

$$V_i = \frac{T_1 + T_0}{T_1} \cdot V_c \quad (11)$$

Adding ( $T_1 \cdot T_0$ ) to numerator and denominator in equation No. 11, we get

$$V_i = \frac{(T_1 + T_0 + T_1 - T_0)}{(T_1 + T_1 - T_0)} \cdot V_c$$

$$V_i = \frac{(2T_1)}{2T_1 - T_0} \cdot V_c \quad (12)$$

Now divide equation No 12 by  $(T_1, T_0)$  to numerator and denominator

$$V_i = \frac{\frac{2(T_1)}{(T_1 - T_0)}}{\left(\frac{(T_1)}{(T_1 - T_0)} + 1\right)} \cdot V_c$$

$$V_i = \frac{2B}{B + 1} \cdot V_c \quad (13)$$

The output peak phase voltage from the inverter can be expressed as

$$V_{ac} = M \cdot \frac{V_i}{2} \quad (14)$$

Where M is the modulation index

For Z- source inverter

$$V_{ac} = M \cdot B \cdot \frac{V_0}{2}$$

$$V_{ac} = B_B \cdot \frac{V_0}{2} \quad (15)$$

The output voltage can be stepped up and down by choosing an appropriate buck - boost factor  $B_B$ .

$$B_B = B \cdot M \text{ (it varies from 0 to } \alpha \text{)} \quad (16)$$

The capacitor voltage can be expressed as

$$V_{c1} = V_{c2} = V_c = \frac{(1 - \frac{T_0}{T}) \cdot V_0}{(1 - 2 \frac{T_0}{T})} \quad (17)$$

The boost factor  $B_B$  is determined by the modulation index m and the boost factor B. The boost factor  $B_B$  can be controlled by duty cycle of the shoot through zero state over the nonshoot through states of the PWM inverter. The shoot through zero state does not affect PWM control of the inverter due to it equivalently produces the same zero voltage to the load terminal. The available shoot through period is limited by the zero state periods that are determined by the modulation index.

### 2.4 Design of Z - network

For the simulation of ZSI, we use following parameters value as given below:

$$L1 = L2 = 160 \mu H.$$

$$C1 = C2 = 1000 \mu F.$$

AC source voltage = 400 V, 60 Hz AC supply

Load : 3 phase, 5.4HP, 440V, 1430 rpm asynchronous machine.

The simulation diagram of Z source inverter fed with AC load as shown in below fig.

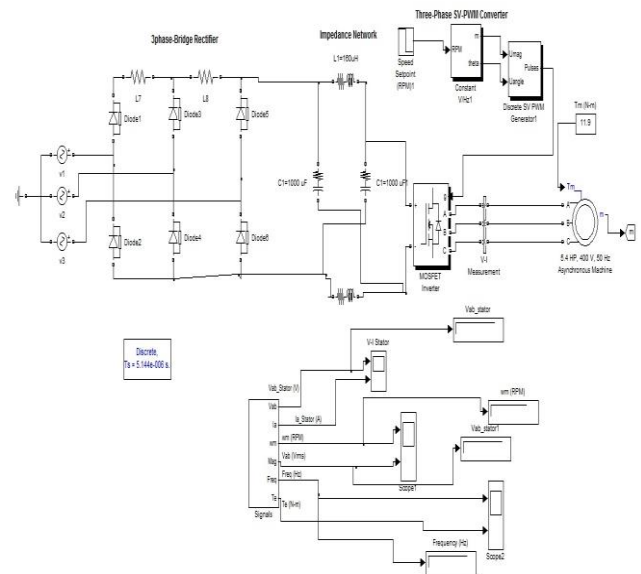
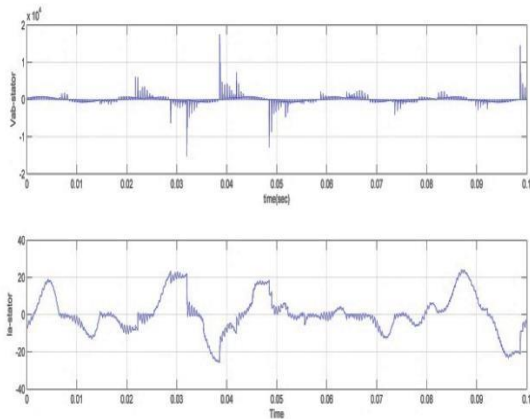


Fig 6: Simulation diagram of ZSI fed with AC load

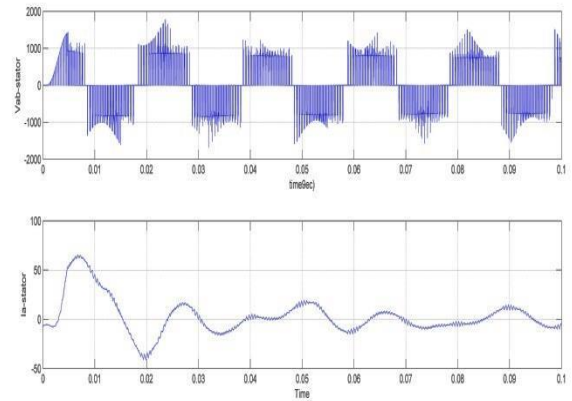
For the improvement in the performance of the AC load such induction motor we need to be considered some output parameter should not get adversely affected. Hence, some of the energy conversion application rectification from AC supply to DC supply by the inversion carried by inverter, where have greater chances of the adverse effect on speed, line voltage, stator voltage, stator current. Similarly, for such parameters we need to calculate precise and suitable values of impedance network elements. For simulation of the proposed system, we use closed loop induction motor drive with constant V/Hz method. This method helps to limit slip speed (being different between synchronous speed and electrical rotor speed), offset voltage and reference speed can be externally adjusted. This external adjustment allows tuning or matching of the induction motor drive to converter and or inverter and tailoring of its characteristics to match load requirement.

### 3. SIMULATION RESULTS:

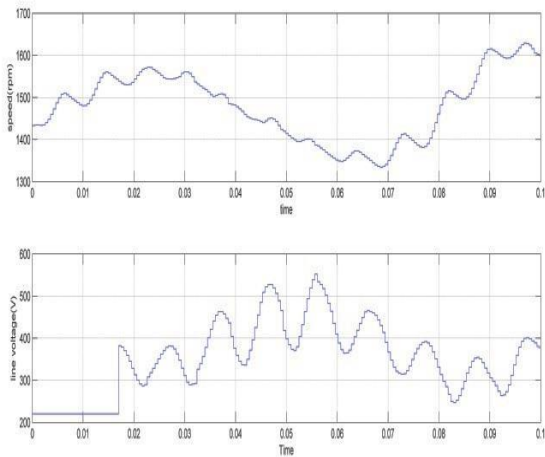
The different parameters of the load which are observed by simulation are shown below



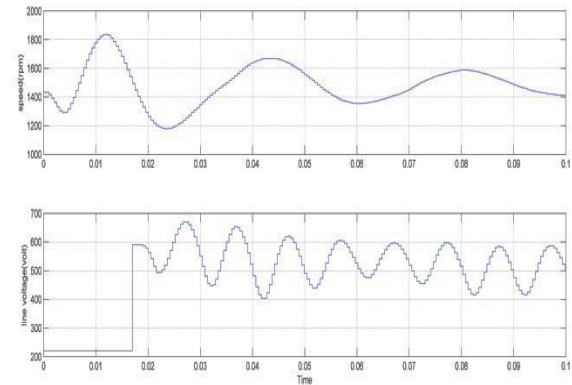
**Fig 7 :**stator voltage and stator current without proposed ZSI



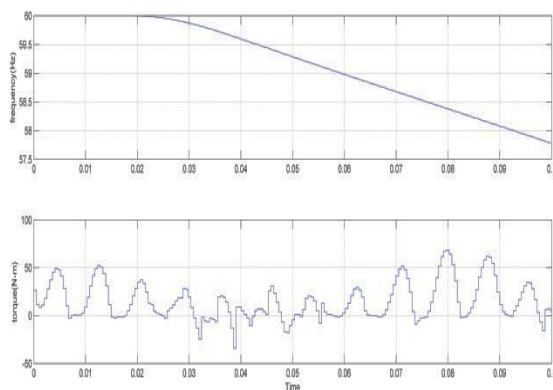
**Fig 10 :**Stator voltage and stator current with proposed ZSI.



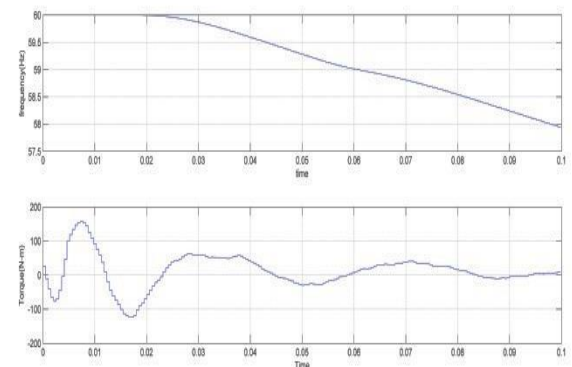
**Fig 8:** Angular speed (rpm) and line voltage without proposed ZSI



**Fig 11 :** Angular speed (rpm) and line voltage with proposed ZSI



**Fig 9 :**Frequency and torque without ZSI



**Fig 9 :** Frequency and torque without ZSI

According to the simulation results ,the speed of motor cannot control precisely in VSI.The rotor speed is less than that of the synchronous speed, thus stator frequency and synchronous speed is variable in VSI.Whereas,according to the simulation of ZSI the speed remains constant.Motor drive runs at below the rated speed of the drive.Hence stator frequency remains constant.

For VSI, due to speed variation, stator current exceeds rated current by many times, thus endangering the inverter-converter combination. Due to symmetrical combination of capacitors and inductors stator current does not exceed rated current therefore there is lowering possibilities of endangering the inverter-converter combination.

Torque is unable to behave transiently due to the oscillations in the rotor flux linkage, results in larger stator current and torque transients in the case of the VSI. Whereas in the case of ZSI torque behaves transiently when motor achieves the synchronous speed.

Stator voltage waveform of the VSI rich in the harmonics. These time harmonics produce rotor current harmonics which turn interact with fundamental air gap flux, generating harmonics torque pulsation which are undesirable which may lead to audible noise and speed pulsation. These can be avoided in the ZSI.

According to the simulation results shown above the following parameters were observed numerically as shown in table-1

**Table -1:** Result Table

Simulation Result table			
Parameters observed by simulation with VSI		Parameters observed by simulation with ZSI	
Angular speed (rpm)	1624	Angular speed (rpm)	1419
Stator Voltage (volts)	350	Stator Voltage (volts)	577.49
Frequency (Hz)	57.84	Frequency (Hz)	58.00
Total Harmonics Distortion(%)	55.98	Total Harmonics Distortion(%)	55.85

#### 4. CONCLUSIONS

The simulation of z source inverter with traditional voltage source inverter are compared with different parameters. According to that, ZSI improves the performance of the induction motor drive with the help of implementation of the constant V/Hz control strategy for Z-source inverter fed induction motor in closed loop control. The barriers present in traditional inverter strategies can be overcome by using ZSI which are shown by the results.

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