

Miniaturization of Microstrip Patch Antenna for Mobile Application

Amit Rakholiya¹, prof. Namrata Langhnoja², Akash Dungrani³

¹P.G. student, Department of Communication System Engineering, L.D.C.E., Ahmedabad, Gujarat, India

²Professor, Department of Communication System Engineering, L.D.C.E., Ahmedabad, Gujarat, India

³P.G. student, Department of Communication System Engineering, L.D.C.E., Ahmedabad, Gujarat, India

Abstract – Fractal Geometry property is used in this paper for the miniaturization of microstrip patch antenna. The unique space filling property of fractals is used to develop novel patch antenna at 1.575 GHz (L1 band) with significant reduced size than its conventional antenna. An inset fed antenna is taken as conventional base antenna. The fractal iteration of Sierpinski carpet in modified form is performed up to 3rd iteration. The antennas are designed using FEM (finite element method) HFSS 13.0 and Return Loss, Radiation Pattern, antenna gain and VSWR are demonstrated. Simulation results of Sierpinski carpet iterations and their comparison with the conventional base antenna shows size reduction of 31% in to 3rd iteration, without affecting antenna performances such as Return Loss, Impedance bandwidth and impedance matching.

Key Words: Fractal antenna, inset-fed, Sierpinski carpet, Miniaturization, Microstrip patch antenna

1. INTRODUCTION

Microstrip patch antenna (MPA) has become an integral part of wireless communication system because of their advantages such as low profile, light weight, available with planner and non-planner structure, simple and economical to manufacturer using modern printed circuit technology and integration with feed networks become easy. The increasing growth of wireless system requires miniaturized antenna. The conventional dimensions of MPA are around half wavelength and therefor, several miniaturization techniques are emerging to reduce these dimensions. The effective techniques to miniature antenna size are: use of high permittivity substrate [1], use of magnetic substrate [2], and increase in electrical length [3], and using shorting pins between patch and ground plane. Apart from these techniques and unlike Euclidean geometry is introduced in antenna designing for size reduction of patch antenna.

The term fractal (broken or irregular segments) was originally introduced by French mathematician Mandelbrot. The complexity of nature is captured by fractals than Euclidean geometry. Fractals are made from multiple iterations of a single geometrical shape. The fundamental

building blocks are scaled version of fractal shape. The application of fractals are such as image compression algorithm, whether prediction, integrated circuits, filter design and many more. Fractal electrodynamics is one of the major applications in which fractal geometry is combined with electromagnetic theory to explore radiation, propagation and scattering problems.

From the different characteristics of the fractal, space filling property is the most important to design an antenna. Different scaled versions of the same copy can be understand by self-similarity and self-affinity property of fractals. Image is reduced by same factor in all directions in self-similarity while self-affinity has different scale factor in different directions. Another important property of fractals is space filling which makes electrically longer them in compact space [4]. It has been observed that self-similar or self-affine property of fractals is useful to design multiband frequency antenna and space-filling property of fractals is useful to design small antenna such as Sierpinski carpet.

Fractal antennas can be designed in many different shapes like Sierpinski carpet, Sierpinski gasket, Minkowski loop and Koch island etc. various design shapes have been used to reduced size of antenna by using fractals on the patch antenna. Koch shaped fractal is used on the patch surface with RO4003 substrate to reduce size of antenna. A small size microstrip patch antenna is proposed by inserting Sierpinski carpet into a single patch and etching inner and outer edge according to Koch curve. The Koch island fractal boundary microstrip antenna has been numerically and experimentally analysed in to obtain considerable area reduction.

The proposed antenna presents the application of fractal geometry to conventional antenna for optimization of its shape to reduce overall size of antenna. By using inset fed patch better impedance matching can be achieved. A simple space-filling fractal, modified Sierpinski carpet [5] with different iterations is etched on inset fed patch antenna to reduce the size than conventional antenna.

2. ANTENNA DESIGN

It is biggest challenge to antenna designers to make antenna with compact size and low cost with simple radiating element, good performance and easy fabrication which demands of today's wireless systems. A modified Sierpinski

carpet patch antenna with inset fed is designed to obtain these results.

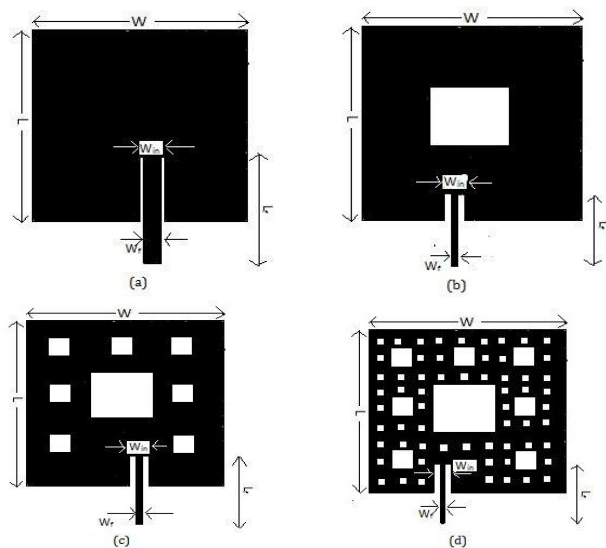


Fig 1. Schematics of designed antennas (a) iteration 0 (b) iteration 1 (c) iteration 2 (d) iteration 3

2.1 Rectangular Patch design

A microstrip patch antenna with inset fed patch is taken as conventional antenna of Sierpinski carpet antenna as shown in figure 1(a). For designing of patch antenna resonant frequency (f_r), dielectric constant (ϵ_r) and thickness of the substrate are the important parameters. Antenna operating frequency is kept as 1.575 GHz which is L1 band frequency used for GPS (Global positioning system) applications in the navigation systems. Antenna substrate material is chosen as FR4 substrate with dielectric constant ϵ_r of 4.4 and loss tangent of 0.02 and thickness of dielectric substrate is chosen as 1.6mm. Analysis is considered based on transmission line model which is simple, accurate and offers good physical insight. Based on this transmission line model, dimensions of rectangular patch antenna are calculated by following given simplified formulation [9].

The width of antenna is determined by

$$W = \frac{c}{2f_r} \sqrt{2/(\epsilon_r + 1)}$$

Since some of the wave travel in the substrate and some in the air, effective dielectric constant is introduced to account for the fringing and is obtained by

$$\epsilon_{eff} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \left[1 + \frac{12h}{w} \right]^{-\left(\frac{2}{3}\right)}$$

Due to the fringing effect, length of the patch is extended on each end by a distance ΔL , which is given by

$$\Delta L = 0.412 \frac{h(\epsilon_{eff} + 0.3) \left(\frac{w}{h} + 0.264 \right)}{(\epsilon_{eff} - 0.258) \left(\frac{w}{h} + 0.8 \right)}$$

The actual length of the patch is given by

$$L_{eff} = \frac{c}{2f_r \sqrt{\epsilon_{eff}}}$$

$$\text{And } L = L_{eff} - 2\Delta L$$

$W, L, \epsilon_r, \epsilon_{eff}, C, f_r$ are width of the patch, length of the patch, dielectric constant of the substrate, effective dielectric constant, velocity of light and resonant frequency of antenna respectively.

Table 1. Dimension of proposed Antenna

Antenna	W(mm)	L(mm)	W _i (mm)	W _{in} (mm)	Y ₀ (mm)	L _i (mm)
Iteration0	57.96	44.75	2.98	3.6	16.25	22.44
Iteration1	57	42.15	1	3	7.71	16.73
Iteration2	57	41.6	1	3	9.71	18.73
Iteration3	56.8	41.85	1	3	7.26	15.73

2.2 Feeding technique

Antenna performance depends on which feeding technique and optimum point we are using. There are different feeding techniques available for microstrip patch antenna feeding like microstrip line, coaxial probe, aperture coupling and proximity coupling. Contacting feeding methods are microstrip line and coaxial probe while aperture coupling and proximity coupling are non-contacting feeding methods. In single layer antenna microstrip line and coaxial probe are used because of its simplicity and ease of fabrication. The impedance matching in microstrip line can be obtained by edge feeding, inset feeding and feeding through additional matching network such as quarter wave length transformer. In this proposed work microstrip line with inset feeding method is used. Due to introduced physical notch junction capacitance appears. These physical notch and junction capacitance affects resonance frequency. As the inset feed point moves from edge to Centre resonant impedance decrease monotonically and reaches zero at the Centre. Basically input impedance depends on the length and width of the notch [10].

The inset distance is calculated by using

$$R_{50} = R_{in} \cos^2 \left(\frac{\pi y_0}{L} \right)$$

Where R_{50}, R_{in} and y_0 are 50 Ω resistance, input impedance of antenna and inset distance respectively.

Table 2. Size reduction

Antenna	Area (mm ²)	Size reduction
Iteration 0	2602.08	
Iteration 1	2252.15	13.45%
Iteration 2	1992.65	23.43%
Iteration 3	1818.23	31%

2.3 GROUND DIMENSIONS

The transmission model is applicable to infinite ground but for practical considerations finite ground plane is used. However, size of the ground plane should be greater than the patch dimensions by approximately six times the substrate thickness so that the results are similar to infinite ground plane.

$$L_g = 6h+L \text{ and}$$

$$W_g = 6h+W$$

L_g and W_g are length of the ground plane and width of the ground plane respectively.

2.4 FRACTAL DESIGN

The space-filling property of the fractal causes effectively increase the electrical length which is used to reduce the size of the antenna. In this paper Sierpinski carpet fractal method with different iterations is used to reduce the size of the patch of the microstrip patch antenna. In this design initially patch is divided into nine congruent rectangles and central rectangle is removed. In the further iterations remaining eight rectangle are again divided into another nine congruent rectangles and from these rectangles again central rectangle is removed. This similar procedure is followed for other iterations. This design is not printed for the whole part of design. The part containing feed line is not disturbed by the Sierpinski carpet fractal to get better impedance matching [8].

The iterative process is based on the following rules

$$N_n = 8^n$$

$$L_n = \left(\frac{1}{3}\right)^n$$

$$A_n = \left(\frac{8}{9}\right)^n$$

Where N_n is the number of rectangles covering the radiating material, L_n is the length ratio, A_n is the ratio for fractal area.

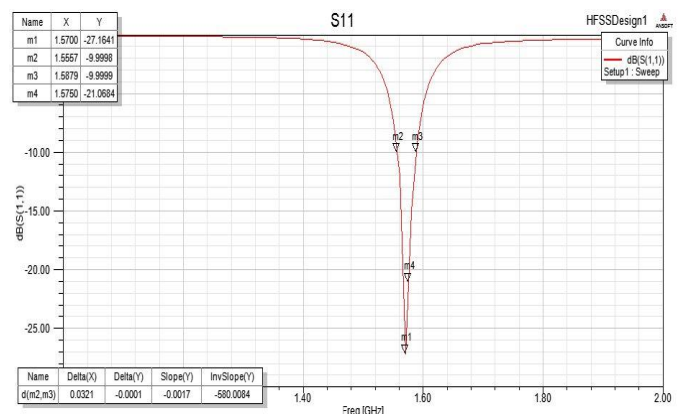
3. SIMULATION AND RESULTS

The antenna was simulated in FEM (Finite Element Method) based Ansoft HFSS 13.0 which is a high performance full wave electromagnetic field simulator for arbitrary 3D graphical user interface. HFSS is the first to use of FEM for EM simulation by implementing technologies such as tangential .vector finite elements, adaptive meshing and adaptive lanczos-pade sweep (ALPS). The Sierpinski carpet iteration was performed up to 3rd iteration as per procedure mentioned earlier.

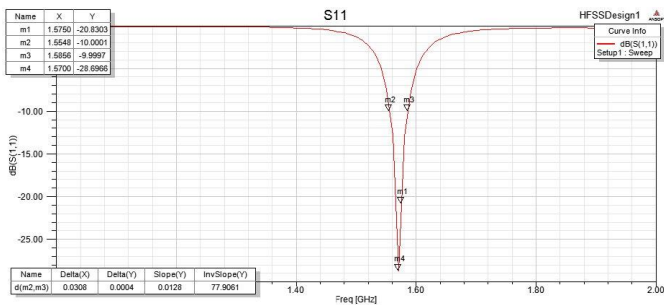
Table 3. Outputs of proposed antenna

Antenna	Iteration0	Iteration1	Iteration2	Iteration3
Return Loss dB	-21.07	-20.83	-22.17	-26.45
Impedance bandwidth	32.1 MHz	30.8 MHz	30.2 MHz	30.5 MHz
BW (%)	2.03 %	1.96 %	1.92 %	1.94 %
Gain (dB)	3.58	3.16	3.1	3.001
VSWR	1.2630	1.3281	1.3286	1.3189

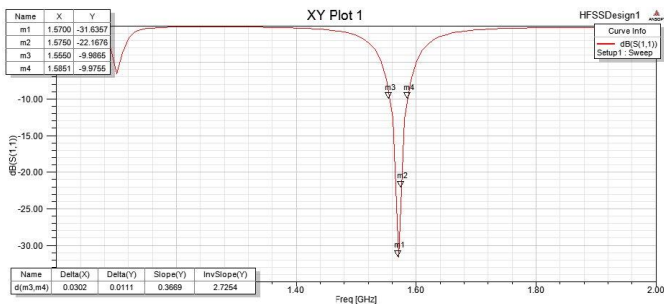
Antenna designed with calculated values did not resonate at 1.575 GHz so parametric analysis of HFSS 13.0 is used to make antenna resonate at desired frequency with impedance of 50 Ω . It is observed that the size of antenna is decreases with increasing order of iteration. The size reduction of 13.45%, 23.43% and 31% is achieved after 1st, 2nd and 3rd iteration. Return loss, Impedance bandwidth, antenna gain and VSWR are evaluated for antenna performance.



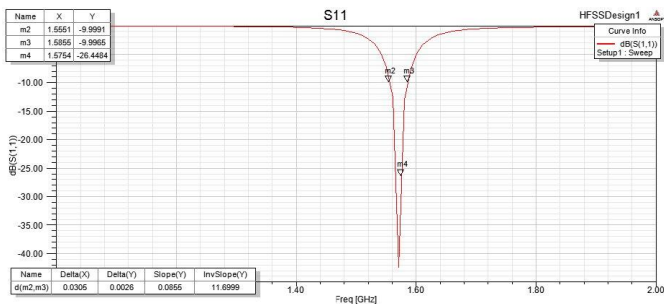
(a)



(b)



(c)



(d)

Fig 2. Simulated Return Loss of (a) iteration 0 (b) iteration 1 (c) iteration 2 (d) iteration 3

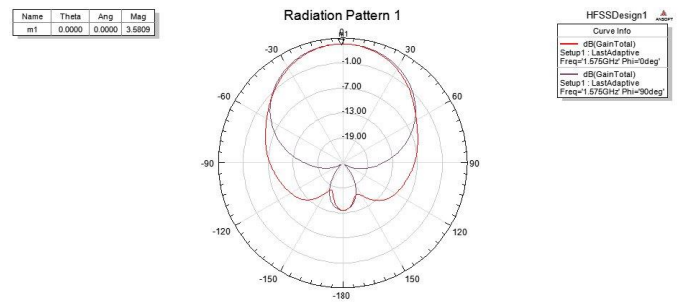
The simulated Return loss of conventional antenna, iteration 0, iteration 1, iteration 2, iteration 3 shown in fig 2(a), fig2 (b), fig2 (c), and fig2 (d) respectively.

Impedance bandwidth is the difference between frequencies at -10 dB and bandwidth in terms of percentage is defined by

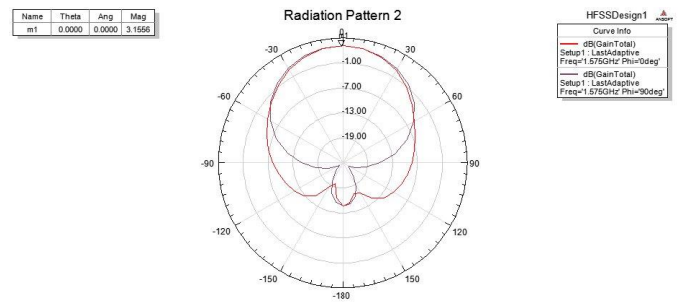
$$BW(\%) = 100 \times \frac{f_{max} - f_{min}}{f_r}$$

Where f_{max} and f_{min} are determined at -10dB.

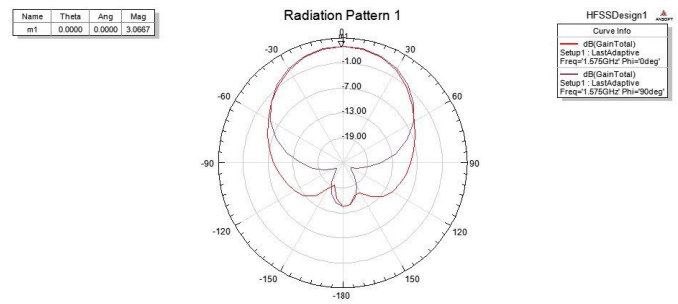
The obtained return loss and impedance bandwidth are shown in table III. It is seen that all the antennas have promising return loss and all are less than -20 dB.



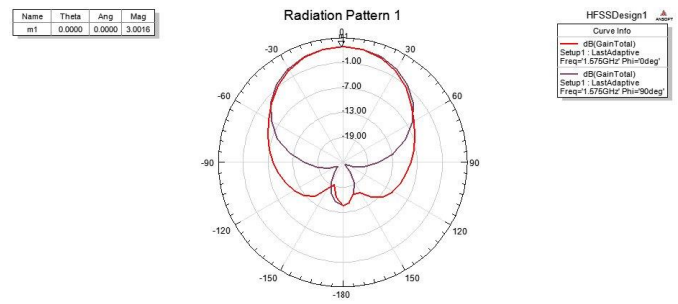
(a)



(b)



(c)

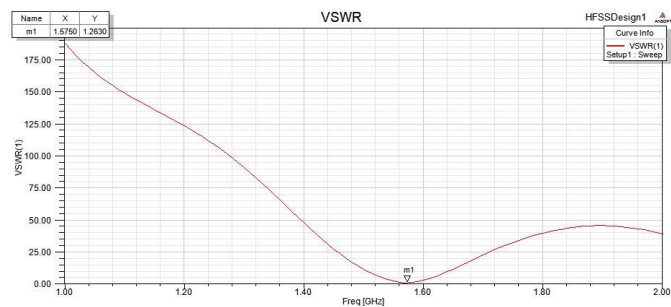


(d)

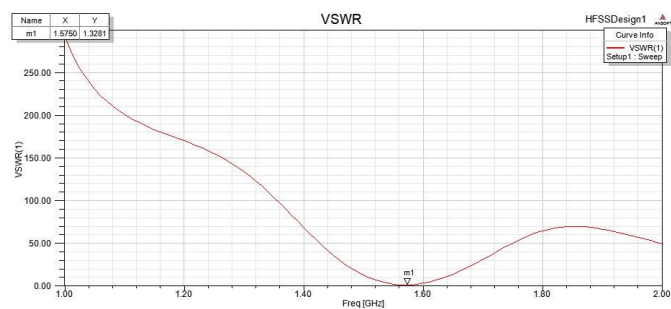
Fig 3. Radiation pattern of (a) iteration 0 (b) iteration 1 (c) iteration 2 (d) iteration 3

The radiation pattern for the conventional antenna and modified Sierpinski carpet fractal antennas for all iteration shown in fig3(a), fig3(b), fig3(c), fig3(d) respectively for phi=90 degree and phi=0 degree. The maximum gain achieved by each antenna is shown in table 3. Conventional

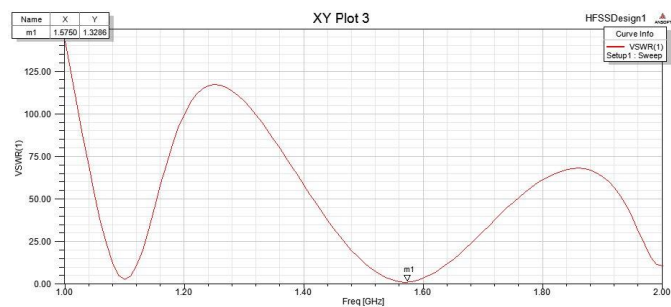
antenna have highest gain of 3.58 dB and its iterations have slightly reduced gain.



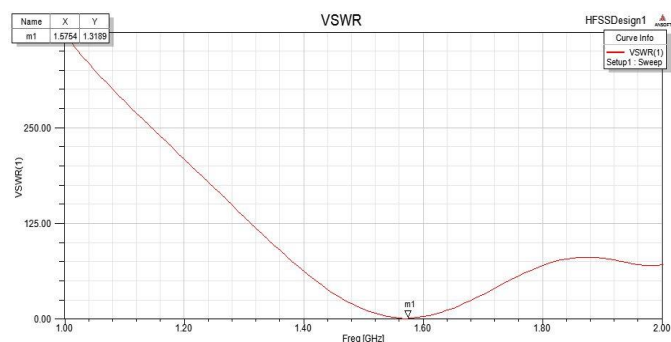
(a)



(b)



(c)



(d)

Fig 4. VSWR of (a) iteration 0 (b) iteration 1 (c) iteration 2 (d) iteration 3

4. CONCLUSION

In this paper, Sierpinski carpet fractal patch antenna operating at a single L1 band frequency (1.575 GHz) having very significant size reduction is presented. The proposed design uses 50 Ω transmission feeding line for the impedance matching with the antenna. The size reduction is achieved through the fractal iterations up to 3rd order without affecting antenna performance such as return loss, impedance bandwidth, antenna gain and VSWR.

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